

Course No.	Course Name	L-T-P - Credits	Year of Introduction
MA202	Probability distributions, Transforms and Numerical Methods	3-1-0-4	2016
Prerequisite: Nil			
Course Objectives			
<ul style="list-style-type: none"> To introduce the concept of random variables, probability distributions, specific discrete and continuous distributions with practical application in various Engineering and social life situations. To know Laplace and Fourier transforms which has wide application in all Engineering courses. To enable the students to solve various engineering problems using numerical methods. 			
Syllabus			
Discrete random variables and Discrete Probability Distribution. Continuous Random variables and Continuous Probability Distribution. Fourier transforms. Laplace Transforms. Numerical methods-solution of Algebraic and transcendental Equations, Interpolation. Numerical solution of system of Equations. Numerical Integration, Numerical solution of ordinary differential equation of First order.			
Expected outcome .			
After the completion of the course student is expected to have concept of (i) Discrete and continuous probability density functions and special probability distributions. (ii) Laplace and Fourier transforms and apply them in their Engineering branch (iii) numerical methods and their applications in solving Engineering problems.			
Text Books:			
<ol style="list-style-type: none"> Miller and Freund's "Probability and statistics for Engineers"-Pearson-Eighth Edition. Erwin Kreyszig, "Advanced Engineering Mathematics", 10th edition, Wiley, 2015. 			
References:			
<ol style="list-style-type: none"> V. Sundarapandian, "Probability, Statistics and Queuing theory", PHI Learning, 2009. C. Ray Wylie and Louis C. Barrett, "Advanced Engineering Mathematics"-Sixth Edition. Jay L. Devore, "Probability and Statistics for Engineering and Science"-Eight Edition. Steven C. Chapra and Raymond P. Canale, "Numerical Methods for Engineers"-Sixth Edition-Mc Graw Hill. 			
Course Plan			
Module	Contents	Hours	Sem. Exam Marks
I	Discrete Probability Distributions. (Relevant topics in section 4.1,4,2,4.4,4.6 Text1)		
	Discrete Random Variables, Probability distribution function, Cumulative distribution function.	2	
	Mean and Variance of Discrete Probability Distribution.	2	
	Binomial Distribution-Mean and variance.	2	
	Poisson Approximation to the Binomial Distribution. Poisson distribution-Mean and variance.	2	
			15%

II	Continuous Probability Distributions. (Relevant topics in section 5.1,5.2,5.5,5.7 Text1)		
	Continuous Random Variable, Probability density function, Cumulative density function, Mean and variance.	2	
	Normal Distribution, Mean and variance (without proof).	4	
	Uniform Distribution.Mean and variance.	2	
	Exponential Distribution, Mean and variance.	2	
FIRST INTERNAL EXAMINATION			
III	Fourier Integrals and transforms. (Relevant topics in section 11.7, 11.8, 11.9 Text2)		15%
	Fourier Integrals. Fourier integral theorem (without proof).	3	
	Fourier Transform and inverse transform.	3	
	Fourier Sine & Cosine Transform, inverse transform.	3	
IV	Laplace transforms. (Relevant topics in section 6.1,6.2,6.3,6.5,6.6 Text2)		15%
	Laplace Transforms, linearity, first shifting Theorem.	3	
	Transform of derivative and Integral, Inverse Laplace transform, Solution of ordinary differential equation using Laplace transform.	4	
	Unit step function, second shifting theorem.	2	
	Convolution Theorem (without proof).	2	
	Differentiation and Integration of transforms.	2	
SECOND INTERNAL EXAMINATION			
V	Numerical Techniques. (Relevant topics in section.19.1,19.2,19.3 Text2)		20%
	Solution Of equations by Iteration, Newton- Raphson Method.	2	
	Interpolation of Unequal intervals-Lagrange's Interpolation formula.	2	
	Interpolation of Equal intervals-Newton's forward difference formula, Newton's Backward difference formula.	3	
VI	Numerical Techniques. (Relevant topics in section 19.5,20.1,20.3, 21.1 Text2)		20%
	Solution to linear System- Gauss Elimination, Gauss Seidal Iteration Method.	3	
	Numeric Integration-Trapezoidal Rule, Simpson's 1/3 Rule.	3	
	Numerical solution of firstorder ODE-Euler method, Runge-Kutta Method (fourth order).	3	
END SEMESTER EXAM			

QUESTION PAPER PATTERN:

Maximum Marks : 100

Exam Duration: 3 hours

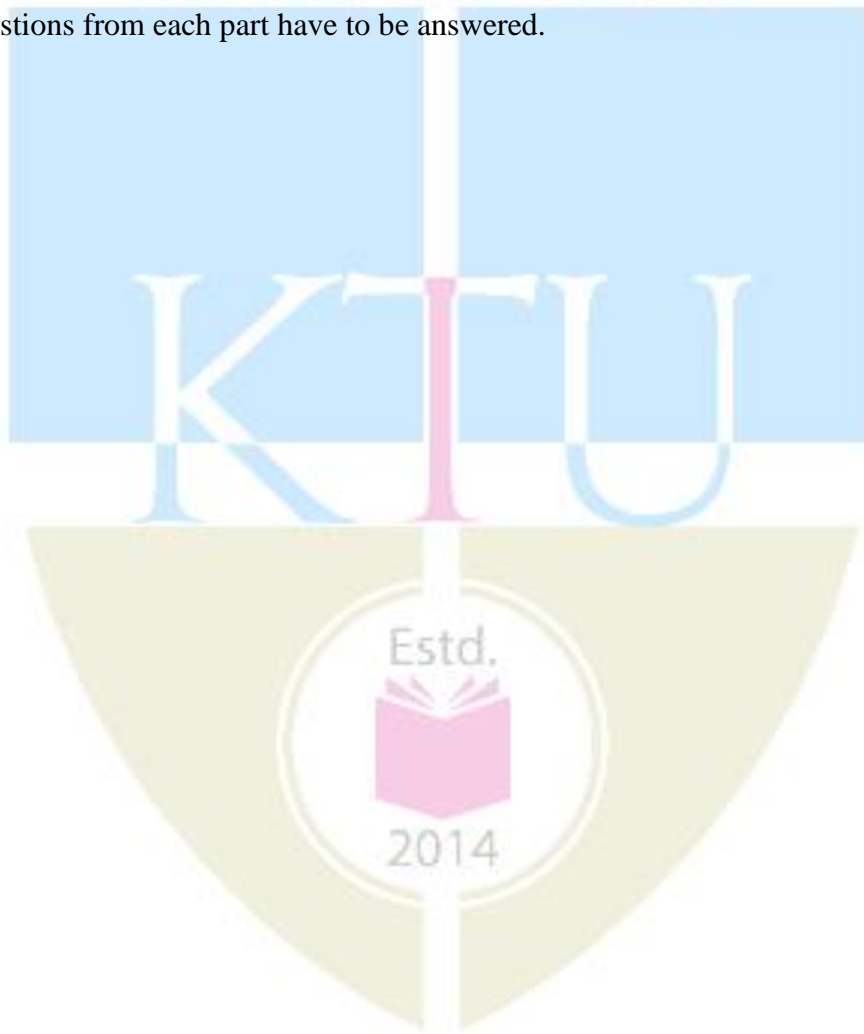
The question paper will consist of 3 parts.

Part A will have 3 questions of 15 marks each uniformly covering modules I and II. Each question may have two sub questions.

Part B will have 3 questions of 15 marks each uniformly covering modules III and IV. Each question may have two sub questions.

Part C will have 3 questions of 20 marks each uniformly covering modules V and VI. Each question may have three sub questions.

Any two questions from each part have to be answered.



Course code	Course Name	L-T-P -Credits	Year of Introduction
CS202	Computer Organization and Architecture	3-1-0-4	2016
Pre-requisite: CS203 Switching theory and logic design			
Course Objectives			
<ol style="list-style-type: none"> To impart an understanding of the internal organization and operations of a computer. To introduce the concepts of processor logic design and control logic design. 			
Syllabus			
Fundamental building blocks and functional units of a computer. Execution phases of an instruction. Arithmetic Algorithms. Design of the processing unit – how arithmetic and logic operations are performed. Design of the control unit – hardwired and microprogrammed control. I/O organisation – interrupts, DMA, different interface standards. Memory Subsystem – different types.			
Expected outcome			
Students will be able to:			
<ol style="list-style-type: none"> identify the basic structure and functional units of a digital computer. analyze the effect of addressing modes on the execution time of a program. design processing unit using the concepts of ALU and control logic design. identify the pros and cons of different types of control logic design in processors. select appropriate interfacing standards for I/O devices. identify the roles of various functional units of a computer in instruction execution. 			
Text Books:			
<ol style="list-style-type: none"> Hamacher C., Z. Vranesic and S. Zaky, <i>Computer Organization</i> ,5/e, McGraw Hill, 2011. Mano M. M., <i>Digital Logic & Computer Design</i>, 4/e, Pearson Education, 2013. 			
References:			
<ol style="list-style-type: none"> Mano M. M., <i>Digital Logic & Computer Design</i>, 4/e, Pearson Education, 2013. Patterson D.A. and J. L. Hennessey, <i>Computer Organization and Design</i>, 5/e, Morgan Kauffmann Publishers, 2013. William Stallings, <i>Computer Organization and Architecture: Designing for Performance</i>, Pearson, 9/e, 2013. Chaudhuri P., <i>Computer Organization and Design</i>, 2/e, Prentice Hall, 2008. Rajaraman V. and T. Radhakrishnan, <i>Computer Organization and Architecture</i>, Prentice Hall, 2011. Messmer H. P., <i>The Indispensable PC Hardware Book</i>, 4/e, Addison-Wesley, 2001 			
Course Plan			
Module	Contents	Hours (51)	Sem.ExamMarks
I	Basic Structure of computers –functional units – basic operational concepts –bus structures – software. Memory locations and addresses – memory operations – instructions and instruction sequencing – addressing modes – ARM Example (programs not required). Basic I/O operations – stacks subroutine calls.	6	15%

II	<p>Basic processing unit – fundamental concepts – instruction cycle - execution of a complete instruction –multiple- bus organization – sequencing of control signals.</p> <p>Arithmetic algorithms: Algorithms for multiplication and division of binary and BCD numbers — array multiplier —Booth’s multiplication algorithm — restoring and non-restoring division — algorithms for floating point, multiplication and division.</p>	10	15%
FIRST INTERNAL EXAMINATION			
III	<p>I/O organization: accessing of I/O devices – interrupts –direct memory access –buses –interface circuits –standard I/O interfaces (PCI, SCSI, USB)</p>	8	15%
IV	<p>Memory system : basic concepts –semiconductor RAMs –memory system considerations – semiconductor ROMs –flash memory –cache memory and mapping functions.</p>	9	15%
SECOND INTERNAL EXAMINATION			
V	<p>Processor Logic Design: Register transfer logic – inter register transfer – arithmetic, logic and shift micro operations –conditional control statements.</p> <p>Processor organization:–design of arithmetic unit, logic unit, arithmetic logic unit and shifter –status register –processor unit –design of accumulator.</p>	9	20%
VI	<p>Control Logic Design: Control organization – design of hardwired control –control of processor unit –PLA control. Micro-programmed control: Microinstructions –horizontal and vertical micro instructions – micro-program sequencer –micro programmed CPU organization.</p>	9	20%
END SEMESTER EXAM			

Question Paper Pattern:

1. There will be *five* parts in the question paper – A, B, C, D, E
2. Part A
 - a. Total marks : 12
 - b. Four questions each having 3 marks, uniformly covering module I and II; All four questions have to be answered.
3. Part B
 - a. Total marks : 18
 - b. Three questions each having 9 marks, uniformly covering module I and II; Two questions have to be answered. Each question can have a maximum of three subparts
4. Part C
 - a. Total marks : 12
 - b. Four questions each having 3 marks, uniformly covering module III and IV; All four questions have to be answered.
5. Part D
 - a. Total marks : 18
 - b. Three questions each having 9 marks, uniformly covering module III and IV; Two questions have to be answered. Each question can have a maximum of three subparts
6. Part E
 - a. Total Marks: 40
 - b. Six questions each carrying 10 marks, uniformly covering modules V and VI; four questions have to be answered.
 - c. A question can have a maximum of three sub-parts.
7. There should be at least 60% analytical/numerical/design questions..

Course code	Course Name	L-T-P -Credits	Year of Introduction
CS204	Operating Systems	3-1-0-4	2016
Pre-requisite: CS205 Data structures			
Course Objectives <ol style="list-style-type: none"> To impart fundamental understanding of the purpose, structure, functions of operating system. To impart the key design issues of an operating system 			
Syllabus Basic concepts of Operating System, its structure, Process management, inter-process communication, process synchronization, CPU Scheduling, deadlocks, Memory Management, swapping, segmentation, paging, Storage Management - disk scheduling, RAID, File System Interface-implementation. Protection.			
Expected outcome Students will be able to: <ol style="list-style-type: none"> identify the significance of operating system in computing devices. exemplify the communication between application programs and hardware devices through system calls. compare and illustrate various process scheduling algorithms. apply appropriate memory and file management schemes. illustrate various disk scheduling algorithms. appreciate the need of access control and protection in an operating system. 			
Text Book: <ol style="list-style-type: none"> Abraham Silberschatz, Peter B Galvin, Greg Gagne, Operating System Concepts, 9/e, Wiley India, 2015. 			
References: <ol style="list-style-type: none"> Garry Nutt, Operating Systems: 3/e, Pearson Education, 2004 Bhatt P. C. P., An Introduction to Operating Systems: Concepts and Practice, 3/e, Prentice Hall of India, 2010. William Stallings, Operating Systems: Internals and Design Principles, Pearson, Global Edition, 2015. Andrew S Tanenbaum, Herbert Bos, Modern Operating Systems, Pearson, 4/e, 2015. Madnick S. and J. Donovan, Operating Systems, McGraw Hill, 2001. Hanson P. B., Operating System Principle, Prentice Hall of India, 2001. Deitel H. M., An Introduction to Operating System Principles, Addison-Wesley, 1990. 			
Course Plan			
Module	Contents	Hours (52)	Sem. Exam marks

I	<p>Introduction: Functions of an operating system. Single processor, multiprocessor and clustered systems – overview. Kernel Data Structures – Operating Systems used in different computing environments.</p> <p>Operating System Interfaces and implementation - User Interfaces, System Calls – examples. Operating System implementation – approaches. Operating System Structure – Monolithic, Layered, Micro-kernel, Modular. System Boot process.</p>	7	15%
II	<p>Process Management: Process Concept – Processes-States – Process Control Block – Threads. Scheduling – Queues – Schedulers – Context Switching. Process Creation and Termination.</p> <p>Inter Process Communication: Shared Memory, Message Passing, Pipes.</p>	9	15%
FIRST INTERNAL EXAMINATION			
III	<p>Process Synchronization: Critical Section-Peterson's solution. Synchronization – Locks, Semaphores, Monitors, Classical Problems – Producer Consumer, Dining Philosophers and Readers-Writers Problems</p>	9	15%
IV	<p>CPU Scheduling – Scheduling Criteria – Scheduling Algorithms.</p> <p>Deadlocks – Conditions, Modeling using graphs. Handling – Prevention – Avoidance – Detection-Recovery.</p>	8	15%
SECOND INTERNAL EXAMINATION			
V	<p>Memory Management: Main Memory – Swapping – Contiguous Memory allocation – Segmentation – Paging – Demand paging</p>	9	20%
VI	<p>Storage Management: <i>Overview of mass storage structure- disks and tapes. Disk structure – accessing disks.</i> Disk scheduling and management. Swap Space.</p> <p>File System Interface: File Concepts – Attributes – operations – types – structure – access methods. File system mounting. Protection. File system implementation. Directory implementation – allocation methods. Free space Management.</p> <p>Protection– Goals, Principles, Domain. Access Matrix.</p>	10	20%
END SEMESTER EXAM			

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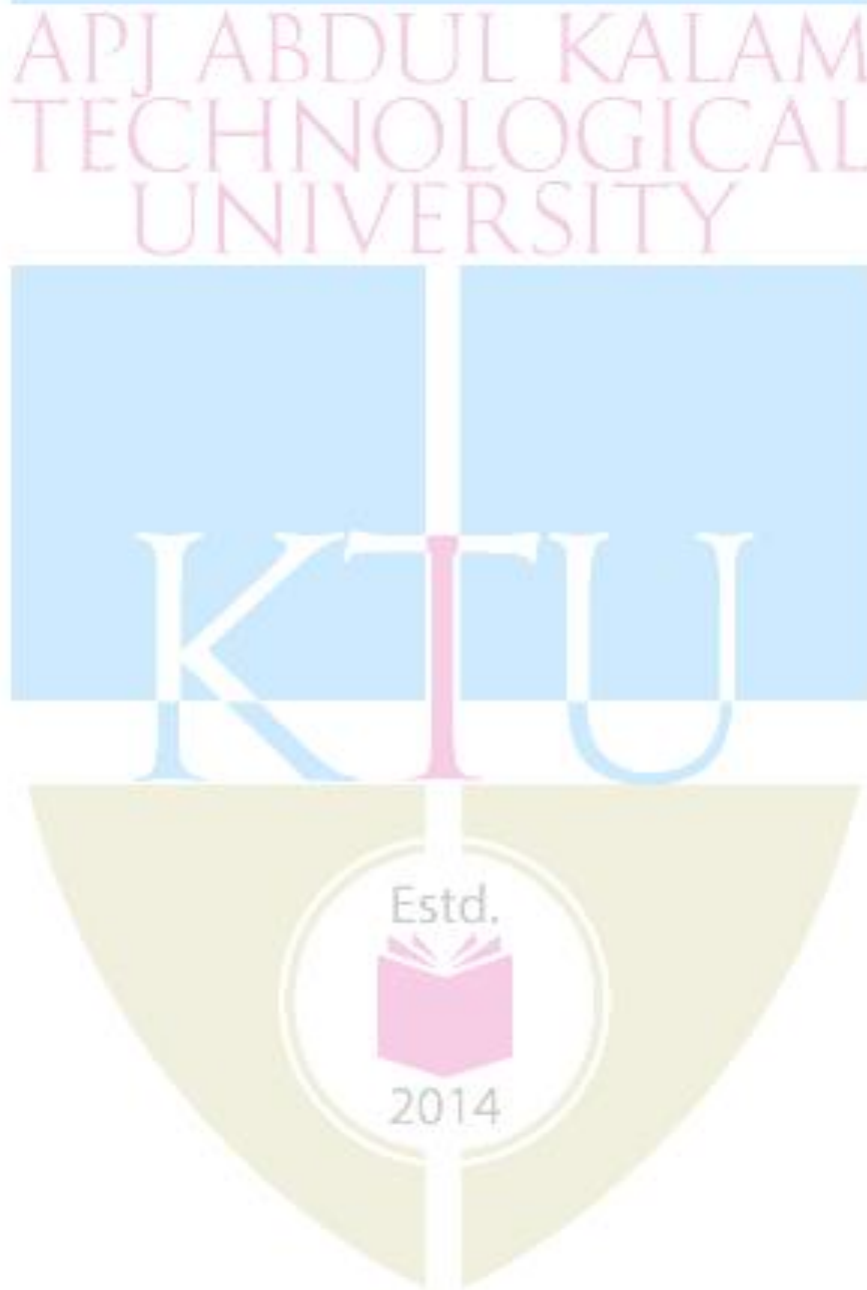
Course code	Course Name	L-T-P - Credits	Year of Introduction
CS206	Object Oriented Design and Programming	2-1-0-3	2016
Pre-requisite: CS205 Data structures			
Course Objectives			
<ol style="list-style-type: none"> To introduce basic concepts of object oriented design techniques. To give a thorough understanding of Java language. To provide basic exposure to the basics of multithreading, database connectivity etc. To impart the techniques of creating GUI based applications. 			
Syllabus			
Object oriented concepts, Object oriented systems development life cycle, Unified Modeling Language, Java Overview, Classes and objects, Parameter passing, Overloading, Inheritance, Overriding, Packages, Exception Handling, Input/Output, Threads and multithreading, Applets, Event Handling mechanism, Working with frames and graphics, AWT Controls, Swings, Java database connectivity.			
Expected outcome.			
Students will be able to:			
<ol style="list-style-type: none"> apply object oriented principles in software design process. develop Java programs for real applications using java constructs and libraries. understand and apply various object oriented features like inheritance, data abstraction, encapsulation and polymorphism to solve various computing problems using Java language. implement Exception Handling in java. use graphical user interface and Event Handling in java. develop and deploy Applet in java. 			
Text Books:			
<ol style="list-style-type: none"> Herbert Schildt, Java: The Complete Reference, 8/e, Tata McGraw Hill, 2011. Bahrami A., Object Oriented Systems Development using the Unified Modeling Language, McGraw Hill, 1999. 			
References:			
<ol style="list-style-type: none"> Y. Daniel Liang, Introduction to Java Programming, 7/e, Pearson, 2013. Nageswararao R., Core Java: An Integrated Approach, Dreamtech Press, 2008. Flanagan D., Java in A Nutshell, 5/e, O'Reilly, 2005. Barclay K., J. Savage, Object Oriented Design with UML and Java, Elsevier, 2004. Sierra K., Head First Java, 2/e, O'Reilly, 2005. Balagurusamy E., Programming JAVA a Primer, 5/e, McGraw Hill, 2014. 			
Course Plan			
Module	Contents	Hours (42)	Sem. ExamMarks
I	Object oriented concepts, Object oriented systems development life cycle. Unified Modeling Language, UML class diagram, Use-case diagram. Java Overview: Java virtual machine, <i>data types</i> , <i>operators</i> , <i>control statements</i> , Introduction to Java programming.	08	15%

II	Classes fundamentals, objects, methods, constructors, parameter passing, overloading, access control keywords.	07	15%
FIRST INTERNAL EXAMINATION			
III	Inheritance basics, method overriding, abstract classes, interface. Defining and importing packages. Exception handling fundamentals, multiple catch and nested try statements.	06	15%
IV	Input/Output: files, stream classes, reading console input. Threads: thread model, use of Thread class and Runnable interface, thread synchronization, multithreading.	06	15%
SECOND INTERNAL EXAMINATION			
V	String class - basics. Applet basics and methods. Event Handling: delegation event model, event classes, sources, listeners.	07	20%
VI	Introduction to AWT: working with frames, graphics, color, font. AWT Control fundamentals. Swing overview. Java database connectivity: JDBC overview, creating and executing queries, dynamic queries.	08	20%
END SEMESTER EXAM			

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Course code	Course Name	L-T-P - Credits	Year of Introduction
CS208	Principles of Database Design	2-1-0-3	2016

Pre-requisite: CS205 Data structures

Course Objectives

- To impart the basic understanding of the theory and applications of database management systems.
- To give basic level understanding of internals of database systems.
- To expose to some of the recent trends in databases.

Syllabus:

Types of data, database and DBMS, Languages and users. Software Architecture, E-R and Extended E-R Modelling, Relational Model – concepts and languages, relational algebra and tuple relational calculus, SQL, views, assertions and triggers, relational db design, FDs and normal forms, Secondary storage organization, indexing and hashing, query optimization, concurrent transaction processing and recovery principles, recent topics.

Expected outcome.

Students will be able to:

1. define, explain and illustrate the fundamental concepts of databases.
2. construct an Entity-Relationship (E-R) model from specifications and to perform the transformation of the conceptual model into corresponding logical data structures.
3. model and design a relational database following the design principles.
4. develop queries for relational database in the context of practical applications
5. define, explain and illustrate fundamental principles of data organization, query optimization and concurrent transaction processing.
6. appreciate the latest trends in databases.

Text Books:

1. Elmasri R. and S. Navathe, *Database Systems: Models, Languages, Design and Application Programming*, Pearson Education, 2013.
2. Silberschatz A., H. F. Korth and S. Sudarshan, *Database System Concepts*, 6/e, McGraw Hill, 2011.

References:

1. Powers S., *Practical RDF*, O'Reilly Media, 2003.
2. Plunkett T., B. Macdonald, *et al.*, *Oracle Big Data Hand Book*, Oracle Press, 2013.

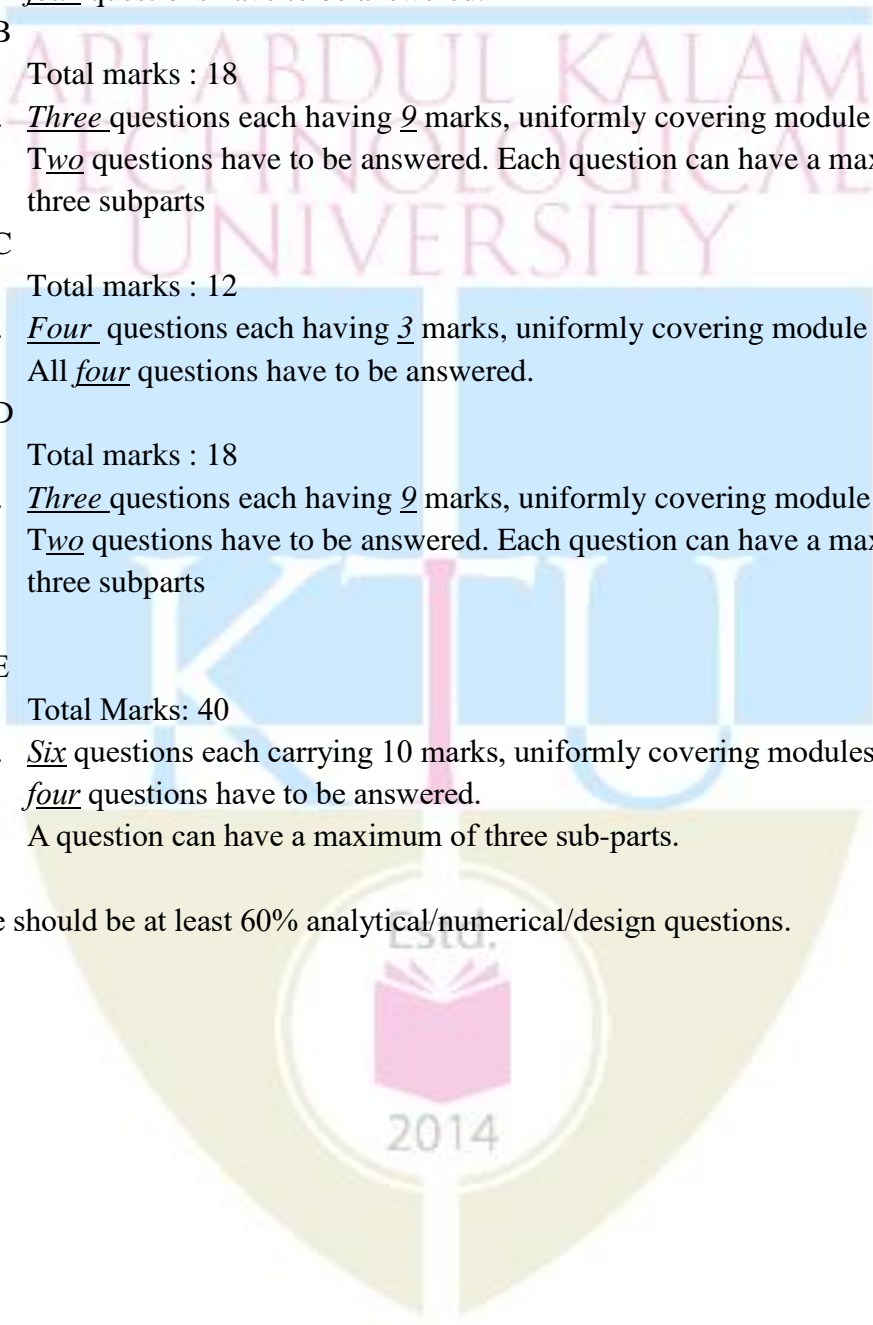
Course Plan

Module	Contents	Hours (42)	Sem. Exam Marks
I	Introduction: Data: structured, semi-structured and unstructured data, Concept & Overview of DBMS, Data Models, Database Languages, Database Administrator, Database Users, Three Schema architecture of DBMS. Database architectures and classification. (Reading: Elmasri Navathe, Ch. 1 and 2. Additional Reading: Silberschatz, Korth, Ch. 1) Entity-Relationship Model: Basic concepts, Design Issues, Mapping Constraints, Keys, Entity-	06	15%

	Relationship Diagram, Weak Entity Sets, Relationships of degree greater than 2 (Reading: Elmasri Navathe, Ch. 7.1-7.8)		
II	Relational Model: Structure of relational Databases, Integrity Constraints, synthesizing ER diagram to relational schema (Reading: Elmasri Navathe, Ch. 3 and 8.1, Additional Reading: Silbershatz, Korth, Ch. 2.1-2.4) Database Languages: Concept of DDL and DML relational algebra (Reading: Silbershatz, Korth, Ch 2.5-2.6 and 6.1-6.2, Elmasri Navathe, Ch. 6.1-6.5)	06	15%
FIRST INTERNAL EXAM			
III	Structured Query Language (SQL): Basic SQL Structure, examples, Set operations, Aggregate Functions, nested sub-queries (Reading: Elmasri Navathe, Ch. 4 and 5.1) Views, assertions and triggers (Reading: Elmasri Navathe, Ch. 5.2-5.3, Optional reading: Silbershatz, Korth Ch. 5.3).	07	15%
IV	Relational Database Design: Different anomalies in designing a database, normalization, functional dependency (FD), Armstrong's Axioms, closures, Equivalence of FDs, minimal Cover (proofs not required). Normalization using functional dependencies, 1NF, 2NF, 3NF and BCNF, lossless and dependency preserving decompositions (Reading: Elmasri and Navathe, Ch. 14.1-14.5, 15.1-15.2. Additional Reading: Silbershatz, Korth Ch. 8.1-8.5)	07	15%
SECOND INTERNAL EXAM			
V	Physical Data Organization: index structures, primary, secondary and clustering indices, Single level and Multi-level indexing, B+-Trees (basic structure only, algorithms not needed), (Reading Elmasri and Navathe, Ch. 17.1-17.4) Query Optimization: heuristics-based query optimization, (Reading Elmasri and Navathe, Ch. 18.1, 18.7)	07	20%
VI	Transaction Processing Concepts: overview of concurrency control and recovery acid properties, serial and concurrent schedules, conflict serializability. Two-phase locking, failure classification, storage structure, stable storage, log based recovery, deferred database modification, check-pointing, (Reading Elmasri and Navathe, Ch. 20.1-20.5 (except 20.5.4-20.5.5) , Silbershatz, Korth Ch. 15.1 (except 15.1.4-15.1.5), Ch. 16.1 – 16.5) Recent topics (preliminary ideas only): Semantic Web and RDF(Reading: Powers Ch.1, 2), GIS, biological databases (Reading: Elmasri and Navathe Ch. 23.3-23.4) Big Data (Reading: Plunkett and Macdonald, Ch. 1, 2)	09	20%
END SEMESTER EXAM			

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Course code	Course Name	L-T-P-Credits	Year of Introduction
HS210	LIFE SKILLS	2-0-2	2016
Prerequisite : Nil			
<p>Course Objectives</p> <ul style="list-style-type: none"> • To develop communication competence in prospective engineers. • To enable them to convey thoughts and ideas with clarity and focus. • To develop report writing skills. • To equip them to face interview & Group Discussion. • To inculcate critical thinking process. • To prepare them on problem solving skills. • To provide symbolic, verbal, and graphical interpretations of statements in a problem description. • To understand team dynamics & effectiveness. • To create an awareness on Engineering Ethics and Human Values. • To instill Moral and Social Values, Loyalty and also to learn to appreciate the rights of others. • To learn leadership qualities and practice them. 			
<p>Syllabus</p> <p>Communication Skill: Introduction to Communication, The Process of Communication, Barriers to Communication, Listening Skills, Writing Skills, Technical Writing, Letter Writing, Job Application, Report Writing, Non-verbal Communication and Body Language, Interview Skills, Group Discussion, Presentation Skills, Technology-based Communication.</p> <p>Critical Thinking & Problem Solving: Creativity, Lateral thinking, Critical thinking, Multiple Intelligence, Problem Solving, Six thinking hats, Mind Mapping & Analytical Thinking.</p> <p>Teamwork: Groups, Teams, Group Vs Teams, Team formation process, Stages of Group, Group Dynamics, Managing Team Performance & Team Conflicts.</p> <p>Ethics, Moral & Professional Values: Human Values, Civic Rights, Engineering Ethics, Engineering as Social Experimentation, Environmental Ethics, Global Issues, Code of Ethics like ASME, ASCE, IEEE.</p> <p>Leadership Skills: Leadership, Levels of Leadership, Making of a leader, Types of leadership, Transactions Vs Transformational Leadership, VUCA Leaders, DART Leadership, Leadership Grid & leadership Formulation.</p>			
<p>Expected outcome</p> <p>The students will be able to</p> <ul style="list-style-type: none"> • Communicate effectively. • Make effective presentations. • Write different types of reports. • Face interview & group discussion. • Critically think on a particular problem. • Solve problems. • Work in Group & Teams • Handle Engineering Ethics and Human Values. • Become an effective leader. 			

Resource Book:

Life Skills for Engineers, Compiled by ICT Academy of Kerala, McGraw Hill Education (India) Private Ltd., 2016

References:

- Barun K. Mitra; (2011), *“Personality Development & Soft Skills”*, First Edition; Oxford Publishers.
- Kalyana; (2015) *“Soft Skill for Managers”*; First Edition; Wiley Publishing Ltd.
- Larry James (2016); *“The First Book of Life Skills”*; First Edition; Embassy Books.
- Shalini Verma (2014); *“Development of Life Skills and Professional Practice”*; First Edition; Sultan Chand (G/L) & Company
- John C. Maxwell (2014); *“The 5 Levels of Leadership”*, Centre Street, A division of Hachette Book Group Inc.

Course Plan

Module	Contents	Hours L-T-P		Sem. Exam Marks
		L	P	
I	Need for Effective Communication, Levels of communication; Flow of communication; Use of language in communication; Communication networks; Significance of technical communication, Types of barriers; Miscommunication; Noise; Overcoming measures,	2		See evaluation scheme
	Listening as an active skill; Types of Listeners; Listening for general content; Listening to fill up information; Intensive Listening; Listening for specific information; Developing effective listening skills; Barriers to effective listening skills.		2	
	Technical Writing: Differences between technical and literary style, Elements of style; Common Errors, Letter Writing: Formal, informal and demi-official letters; business letters, Job Application: Cover letter, Differences between bio-data, CV and Resume, Report Writing: Basics of Report Writing; Structure of a report; Types of reports.		4	
	Non-verbal Communication and Body Language: Forms of non-verbal communication; Interpreting body-language cues; Kinesics; Proxemics; Chronemics; Effective use of body language	3		
	Interview Skills: Types of Interviews; Ensuring success in job interviews; Appropriate use of non-verbal communication, Group Discussion: Differences between group discussion and debate; Ensuring success in group discussions, Presentation Skills: Oral presentation and public speaking skills; business presentations, Technology-based Communication: Netiquettes: effective e-mail messages; power-point presentation; enhancing editing skills using computer software.		4	

<p>II</p>	<p>Need for Creativity in the 21st century, Imagination, Intuition, Experience, Sources of Creativity, Lateral Thinking, Myths of creativity</p> <p>Critical thinking Vs Creative thinking, Functions of Left Brain & Right brain, Convergent & Divergent Thinking, Critical reading & Multiple Intelligence.</p> <p>Steps in problem solving, Problem Solving Techniques, Problem Solving through Six Thinking Hats, Mind Mapping, Forced Connections.</p> <p>Problem Solving strategies, Analytical Thinking and quantitative reasoning expressed in written form, Numeric, symbolic, and graphic reasoning, Solving application problems.</p>	<p>2</p> <p>2</p> <p>2</p>	<p>2</p> <p>2</p> <p>2</p>	
<p>III</p>	<p>Introduction to Groups and Teams, Team Composition, Managing Team Performance, Importance of Group, Stages of Group, Group Cycle, Group thinking, getting acquainted, Clarifying expectations.</p> <p>Group Problem Solving, Achieving Group Consensus.</p> <p>Group Dynamics techniques, Group vs Team, Team Dynamics, Teams for enhancing productivity, Building & Managing Successful Virtual Teams. Managing Team Performance & Managing Conflict in Teams.</p> <p>Working Together in Teams, Team Decision-Making, Team Culture & Power, Team Leader Development.</p>	<p>3</p> <p>3</p> <p>3</p>	<p>2</p> <p>2</p> <p>2</p>	
<p>IV</p>	<p>Morals, Values and Ethics, Integrity, Work Ethic, Service Learning, Civic Virtue, Respect for Others, Living Peacefully.</p> <p>Caring, Sharing, Honesty, Courage, Valuing Time, Cooperation, Commitment, Empathy, Self-Confidence, Character</p> <p>Spirituality, Senses of 'Engineering Ethics', variety of moral issues, Types of inquiry, moral dilemmas, moral autonomy, Kohlberg's theory, Gilligan's theory, Consensus and controversy, Models of Professional Roles, Theories about right action, Self-interest, customs and religion, application of ethical theories.</p> <p>Engineering as experimentation, engineers as responsible experimenters, Codes of ethics, Balanced outlook on.</p> <p>The challenger case study, Multinational corporations, Environmental ethics, computer ethics,</p>	<p>3</p> <p>3</p> <p>3</p>	<p>2</p> <p>2</p> <p>2</p>	

	Weapons development, engineers as managers, consulting engineers, engineers as expert witnesses and advisors, moral leadership, sample code of Ethics like ASME, ASCE, IEEE, Institution of Engineers(India), Indian Institute of Materials Management, Institution of electronics and telecommunication engineers(IETE), India, etc.	3		
V	Introduction, a framework for considering leadership, entrepreneurial and moral leadership, vision, people selection and development, cultural dimensions of leadership, style, followers, crises.	4	2	
	Growing as a leader, turnaround leadership, gaining control, trust, managing diverse stakeholders, crisis management			
	Implications of national culture and multicultural leadership Types of Leadership, Leadership Traits.	2		
	Leadership Styles, VUCA Leadership, DART Leadership, Transactional vs Transformational Leaders, Leadership Grid, Effective Leaders, making of a Leader, Formulate Leadership		2	
END SEMESTER EXAM				

EVALUATION SCHEME

Internal Evaluation

(Conducted by the College)

Total Marks: 100

Part – A

(To be started after completion of Module 1 and to be completed by 30th working day of the semester)

1. Group Discussion – Create groups of about 10 students each and engage them on a GD on a suitable topic for about 20 minutes. Parameters to be used for evaluation is as follows;

- | | | | |
|-------|------------------------|---|----------|
| (i) | Communication Skills | – | 10 marks |
| (ii) | Subject Clarity | – | 10 marks |
| (iii) | Group Dynamics | - | 10 marks |
| (iv) | Behaviors & Mannerisms | - | 10 marks |

(Marks: 40)

Part – B

(To be started from 31st working day and to be completed before 60th working day of the semester)

2. Presentation Skills – Identify a suitable topic and ask the students to prepare a presentation (preferably a power point presentation) for about 10 minutes. Parameters to be used for evaluation is as follows;

(i)	Communication Skills*	-	10 marks
(ii)	Platform Skills**	-	10 marks
(iii)	Subject Clarity/Knowledge	-	10 marks

(Marks: 30)

* Language fluency, audibility, voice modulation, rate of speech, listening, summarizes key learnings etc.

** Postures/Gestures, Smiles/Expressions, Movements, usage of floor area etc.

Part – C

(To be conducted before the termination of semester)

3. Sample Letter writing or report writing following the guidelines and procedures. Parameters to be used for evaluation is as follows;

(i)	Usage of English & Grammar	-	10 marks
(ii)	Following the format	-	10 marks
(iii)	Content clarity	-	10 marks

(Marks: 30)

External Evaluation
(Conducted by the University)

Total Marks: 50

Time: 2 hrs.

Part – A

Short Answer questions

There will be one question from each area (five questions in total). Each question should be written in about maximum of 400 words. Parameters to be used for evaluation are as follows;

- (i) Content Clarity/Subject Knowledge
- (ii) Presentation style
- (iii) Organization of content

(Marks: 5 x 6 = 30)

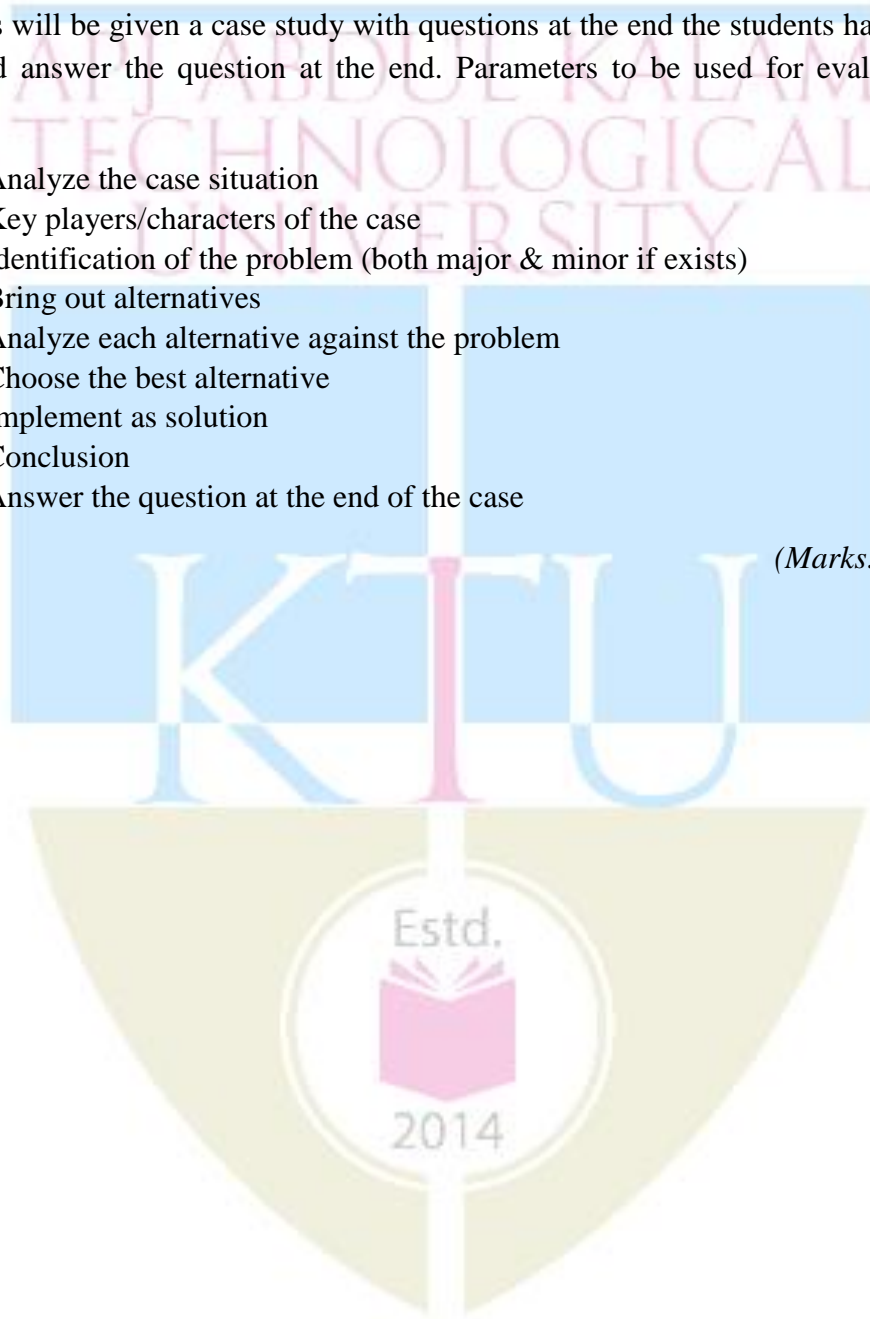
Part – B

Case Study

The students will be given a case study with questions at the end the students have to analyze the case and answer the question at the end. Parameters to be used for evaluation are as follows;

- (i) Analyze the case situation
- (ii) Key players/characters of the case
- (iii) Identification of the problem (both major & minor if exists)
- (iv) Bring out alternatives
- (v) Analyze each alternative against the problem
- (vi) Choose the best alternative
- (vii) Implement as solution
- (viii) Conclusion
- (ix) Answer the question at the end of the case

(Marks: 1 x 20 = 20)



Course code	Course Name	L-T-P-Credits	Year of Introduction
CS232	Free and Open Source Software Lab	0-0-3-1	2016

Pre-requisite: CS204 Operating systems

Course Objectives: To expose students to FOSS environment and introduce them to use open source packages in open source platform.

List of Exercises/Experiments:

1. Getting started with Linux basic commands for directory operations, displaying directory structure in tree format etc.
2. Linux commands for operations such as redirection, pipes, filters, job control, changing ownership/permissions of files/links/directory.
3. Advanced linux commands curl, wget, ftp, ssh and grep
4. Shell Programming : Write shell script to show various system configuration like
 - Currently logged user and his login name
 - Your current shell
 - Your home directory
 - Your operating system type
 - Your current path setting
 - Your current working directory
 - Number of users currently logged in
5. Write shell script to show various system configurations like
 - your OS and version, release number, kernel version
 - all available shells
 - computer CPU information like processor type, speed etc
 - memory information
 - hard disk information like size of hard-disk, cache memory, model etc
 - File system (Mounted)
6. Write a shell script to implement a menu driven calculator with following functions
 1. Addition
 2. Subtraction
 3. Multiplication
 4. Division
 5. Modulus
7. Write a script called addnames that is to be called as follows
./addnames ulist username
Here *ulist* is the name of the file that contains list of user names and *username* is a particular student's username. The script should
 - check that the correct number of arguments was received and print a message, in case the number of arguments is incorrect
 - check whether the ulist file exists and print an error message if it does not
 - check whether the username already exists in the file. If the username exists, print a message stating that the name already exists. Otherwise, add the username to the end of the list.

8. Version Control System setup and usage using GIT. Try the following features.
 - Creating a repository
 - Checking out a repository
 - Adding content to the repository
 - Committing the data to a repository
 - Updating the local copy
 - Comparing different revisions
 - Revert
 - Conflicts and a conflict Resolution
9. Shell script which starts on system boot up and kills every process which uses more than a specified amount of memory or CPU.
10. Introduction to packet management system : Given a set of RPM or DEB, build and maintain, and serve packages over http or ftp. Configure client systems to access the package repository.
11. Perform simple text processing using Perl, Awk.
12. Running PHP : simple applications like login forms after setting up a LAMP stack
13. Virtualisation environment (e.g., xen, kqemu, virtualbox or lguest) to test applications, new kernels and isolate applications. It could also be used to expose students to other alternate OS such as freeBSD
14. Compiling from source : learn about the various build systems used like the auto* family, cmake, ant etc. instead of just running the commands. This could involve the full process like fetching from a cvs and also include autoconf, automake etc.,
15. Kernel configuration, compilation and installation : Download / access the latest kernel source code from *kernel.org*, compile the kernel and install it in the local system. Try to view the source code of the kernel
16. GUI Programming: Create scientific calculator – using any one of Gambas, GTK, QT
17. Installing various software packages. Either the package is yet to be installed or an older version is present. The student can practice installing the latest version. (Internet access is needed).
 - Install samba and share files to windows
 - Install Common Unix Printing System(CUPS)
18. Set up the complete network interface by configuring services such as gateway, DNS, IP tables etc. using *ifconfig*

Expected outcome:

The students will be able to:

1. Identify and apply various Linux commands
2. Develop shell scripts and GUI for specific needs
3. Use tools like GIT
4. Perform basic level application deployment, kernel configuration and installation, packet management and installation etc.

Course code	Course Name	L-T-P - Credits	Year of Introduction
CS234	DIGITAL SYSTEMS LAB	0-0-3-1	2016
Pre-requisite: CS203 Switching theory and logic design			
Course Objectives:			
<ol style="list-style-type: none"> To familiarize students with digital ICs, the building blocks of digital circuits To provide students the opportunity to set up different types of digital circuits and study their behaviour 			
List of Exercises/Experiments : (minimum 12 exercises/experiments are mandatory)			
<ol style="list-style-type: none"> Familiarizations and verification of the truth tables of basic gates and universal gates. Verification of Demorgan's laws for two variables. Implementation of half adder and full adder circuits using logic gates. Implementation of half subtractor and full subtractor circuits using logic gates. Implementation of parallel adder circuit. Realization of 4 bit adder/subtractor and BCD adder circuits using IC 7483. Implementation of a 2 bit magnitude comparator circuit using logic gates. Design and implementation of code convertor circuits a) BCD to excess 3 code b) binary to gray code Implementation of multiplexer and demultiplexer circuits using logic gates. Familiarization with various multiplexer and demultiplexer ICs. Realization of combinational circuits using multiplexer/demultiplexer ICs. Implementation of SR, D, JK, JK master slave and T flip flops using logic gates. Familiarization with IC 7474 and IC 7476. Implementation of shift registers using flip flop Integrated Circuits. Implementation of ring counter and Johnson counter using flip flop Integrated Circuits. Realization of asynchronous counters using flip flop ICs. Realization of synchronous counters using flip flop ICs. Familiarization with various counter Integrated Circuits. Implementation of a BCD to 7 segment decoder and display. Simulation of Half adder, Full adder using VHDL. <p><i>(Note: The experiments may be done using hardware components and/or VHDL)</i></p>			
Course outcome:			
Students will be able to:			
<ol style="list-style-type: none"> identify and explain the digital ICs and their use in implementing digital circuits. design and implement different kinds of digital circuits. 			